

ABSTRACT OF THE DISCLOSURE

A noise eliminating circuit is disclosed which comprises a noise elimination processing unit that interpolates a generation period of pulse noise overlapped with a received signal depending on a first detection signal acquired by level detection of an intermediate frequency signal of the received signal, the first detection signal indicating the generation of the pulse noise, wherein the noise eliminating circuit comprises: a predicting unit that predicts a value of the intermediate frequency signal at a predetermined clock time based on an intermediate frequency signal generated a predetermined time earlier than the intermediate frequency signal; a detecting unit that compares a difference between the value of the predicted intermediate frequency signal and the value of the generated intermediate frequency signal, at the predetermined clock time, with a predetermined threshold, to output a second detection signal indicating the generation of the pulse noise; and a noise elimination controlling unit that selectively outputs the first detection signal and the second detection signal as a signal for interpolating the generation period of the pulse noise to the noise elimination processing unit depending on electric field intensity signal acquired based on the intermediate frequency signal.